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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Lin, et al. Attorney Docket: TSM03-0670
Filed: December 5, 2003 Examiner: Matthew E. Warren
Serial No.: 10/729,092 Art Unit: 2815
For: Structure and Method of Forming Integrated Circuits Utilizing Strained Channel Transistors

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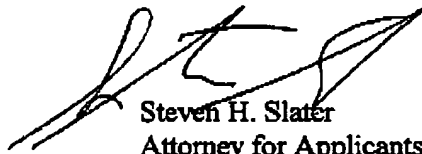
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Respectfully submitted,


Steven H. Slater
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September 29, 2005

Date

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				Application Number	10/729,092
				Filing Date	12/5/2003
				First Named Inventor	Lin, et al.
				Art Unit	2815
				Examiner Name	Matthew E. Warren
				Attorney Docket Number	TSM03-0670
Sheet	1	of	1		

[illegible][illegible]

Examiner Signature		Date Considered	
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 809. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ¹Applicant's unique citation designation number (optional). ²See Kinda Codes of USPTO Patent Documents at www.uspto.gov or MPEP 801.04. ³Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶Applicant is to place a check mark here if English language Translation is attached.

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